

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Kyle K. Kirby

**Serial No.:** 10/673,692

**Filed:** September 29, 2003

**For:** METHOD FOR CREATING  
ELECTRICAL PATHWAYS FOR  
SEMICONDUCTOR DEVICE  
STRUCTURES USING LASER  
MACHINING PROCESSES

**Confirmation No.:** 4168

**Examiner:** M. Estrada

**Group Art Unit:** 2823

**Attorney Docket No.:** 2269-5665US  
(2002-1291.00/US)

**Notice of Allowance Mailed:**

November 30, 2007

VIA ELECTRONIC FILING  
February 25, 2008

**AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

**Amendments to the specification** begin on page 3 of this paper;

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**Amendments to the claims** are set forth in the listing of the claims that begins on page 5 of this paper; and

**Remarks** start at page 12 of this paper.